

EDA TECHNOLOGIES PRESENTS:

HIGH-SPEED DESIGN SEMINAR

WITH THE EXPERT CHUCK CORLEY

Chuck Corley spent 35+ years getting his fingers dirty designing bleeding-edge wired routers, wired & microwave & fiber-optic telecommunications products, and the highest frequency instrumentation used for making microwave measurements.

Chuck was given the nickname "**MacGyver**" for his ability to tackle and solve complex problems that other engineers had given up on.

Virtually every single product Chuck designed included the words "leading edge" and "high frequency". Chuck developed these products at companies including Cisco Systems, Hewlett Packard, National Instruments, Mahi Networks, Agilent Technologies, and Keithley Instruments!

At almost all of these companies Chuck was a primary signal integrity expert and high speed design consultant who taught design classes and techniques to new and experienced engineers.



This course draws substantially from decades of Chuck Corley's hands-on experience.

Chuck Corley works alongside Lee Ritchey at Speeding Edge in California. Getting it "right the first time" is part of their DNA.

***The last day is a bonus covering the topic: 5G PCBs.**

This comprehensive 4-day course covers all key aspects of the high-speed design process. If you can take only one course on this subject, this is the one for you.



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HIGH-SPEED DESIGN SEMINAR

WITH THE EXPERT CHUCK CORLEY

Location:

Centurion, Pretoria

Dates:

7-10 May 2024

Price:

R 25,990.00pp

Topics That Will Be Covered:

- Blind and Buried Vias
- Bypass Capacitors and Inductors
- Calculating Impedance
- Copper Roughness
- Crosstalk
- Design Rule Creation Using Noise Margin Analysis
- Design Tools
- Designing PCB Pad Stacks
- Determining Drilled Hole Dimensions
- Designing the PCB Stackup
- Developing Routing Rules
- Differential Signaling
- Differential Skew & Weave Effect
- EMI
- Gathering Design Data
- Getting to 56 Gb/S
- PAM4 Signaling
- PCB Design
- PCB Dielectric Losses
- PCB Fabrication
- PCB Structures, right angle bends, vias
- PCI Express
- Plane Capacitance
- Post Layout Checking
- Power Delivery System Design
- Receive Equalization
- Reflections
- Review Fundamentals
- Terminations
- Testing Fabricated PCBs
- Transmission Line Definition
- Types of Transmission Lines
- IC Package Effects
- Modeling the PDS
- ***Bonus day 4 covering 5G PCBs***

What Others Attendees Had To Say:

“The content around power distribution systems and effect on EMI was new to me.”

“Chuck's tips on designing good power planes, as well as his guidance on capacitor usage. It's very different from what we usually do.”

“Chuck's insight and teaching has already elevated my ability to produce quality boards. I find the information very applicable and usable to PCB design.”

“It was a good experience to learn from someone who is an expert in the field.”

“Real world examples are always good. I saw a lot of that.”

How To Register:



Email us on:

seminar@edatech.co.za



Call us on:

+27 12 665 0375

*Contact us for early bird discount